

S/N: -----

 Docket No.: UMI-355

 Title: SENSE AMPLIFIER POWER-GATING TECHNIQUE

 FOR INTEGRATED CIRCUIT MEMORY DEVICES AND THOSE

 DEVICES INCORPORATING EMBEDDED DYNAMIC RANDOM

 ACCESS MEMORY (DRAM)

 Inv: Kim C. Hardee

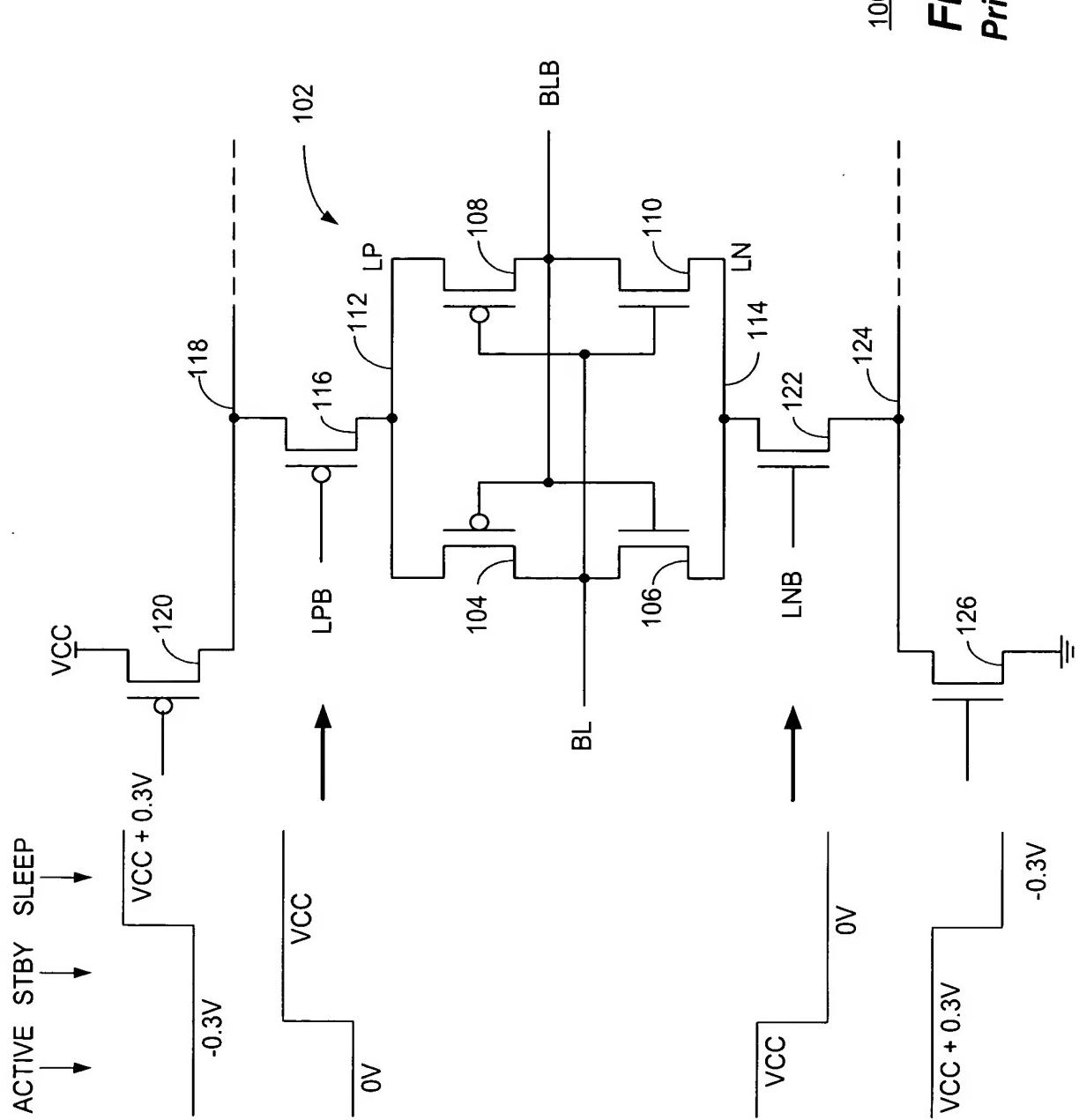


Fig. 1
Prior Art

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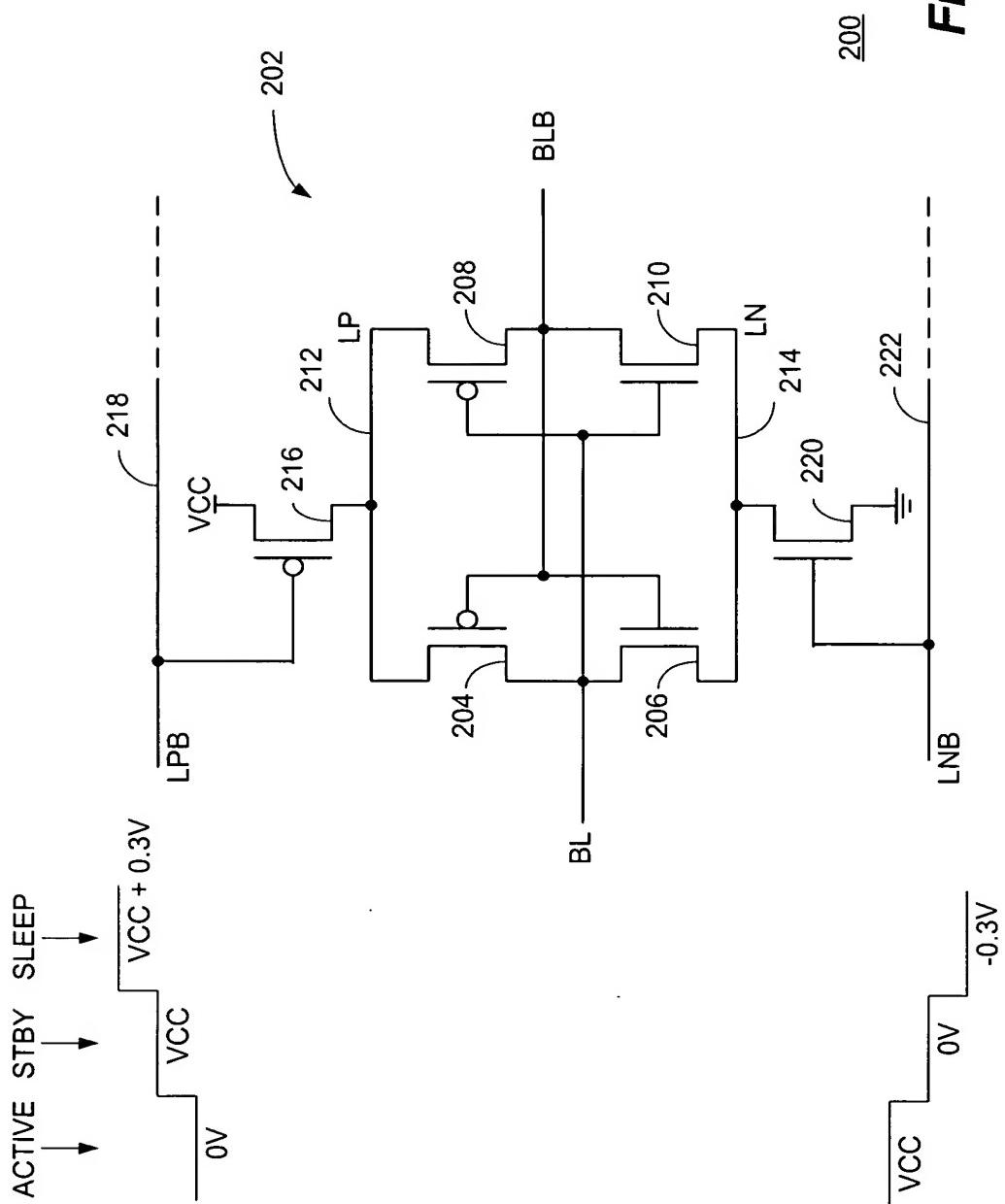


Fig. 2

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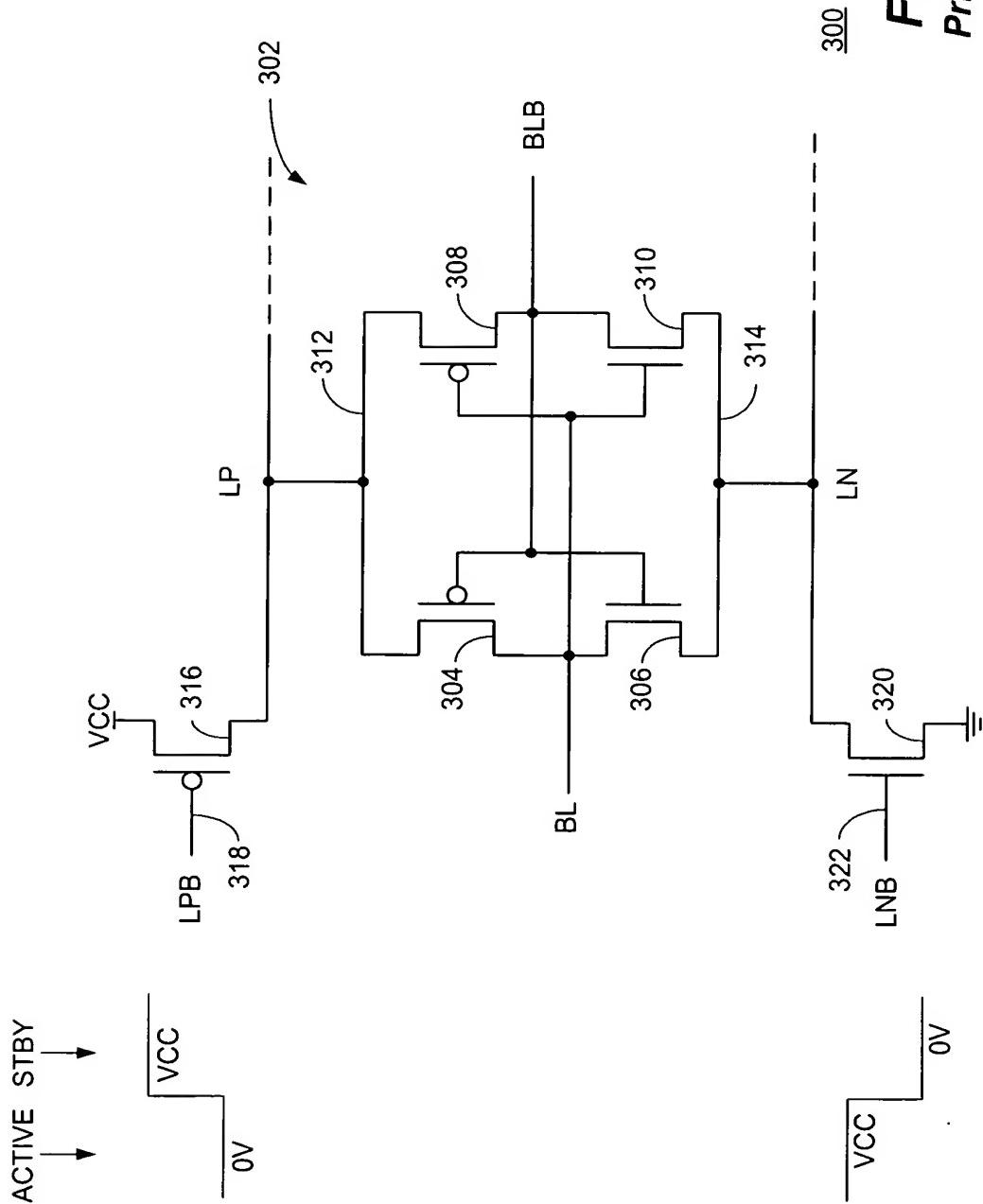


Fig. 3
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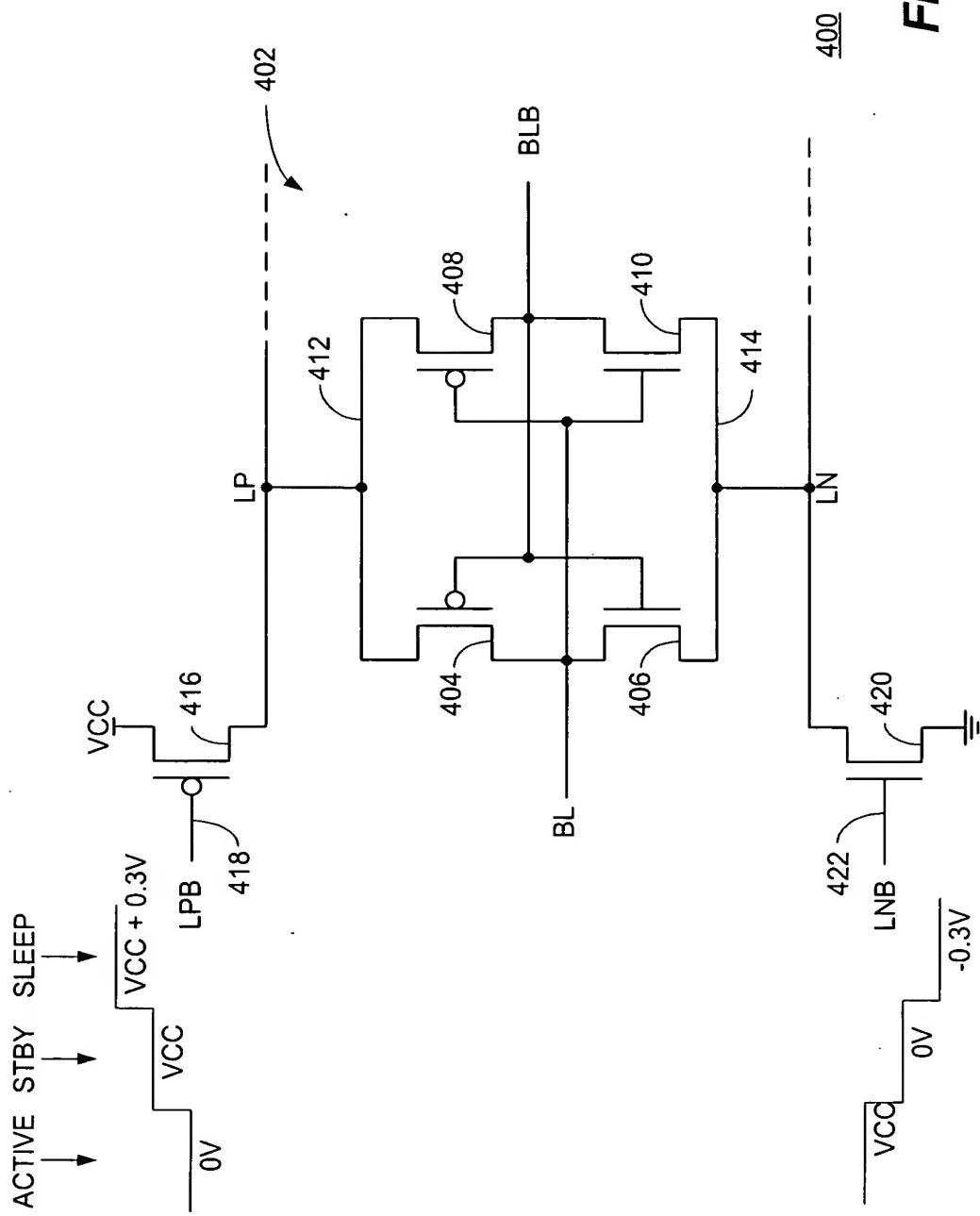


Fig. 4